

Form PTO-1449 (Modified)

U.S. Department of Commerce
Patent and Trademark OfficeAtty. Docket No.
20063/OG03-043Serial No.
10/749,489

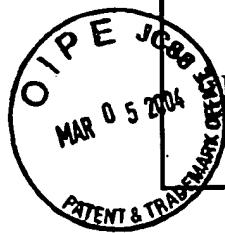
Applicant

Seung Hee JWA

Filing Date
December 30,
2003Group Art Unit
Unknown

INFORMATION DISCLOSURE STATEMENT

(Use several sheets if necessary)



U.S. PATENT DOCUMENTS

*EXAMINER INITIALS		DOCUMENT NUMBER	ISSUE DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
CL	A01	6,566,195	May 20, 2003	Method and Structure for an Improved Floating Gate Memory Cell	438	257	February 6, 2002
CL	A02	6,326,263	December 4, 2001	Method of Fabricating a Flash Memory Cell	438	257	August 11, 2000
CL	A03	6,153,494	November 28, 2000	Method to Increase the Coupling Ration of Word Line to Floating Gate by Lateral Coupling in Stacked Gate of Flash	438	424	May 12, 1999

EXAMINER

calvin lee

DATE CONSIDERED

1/26/05

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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PATENT & TRADEMARK OFFICE

U.S. PATENT DOCUMENTS							
*EXAMINER INITIALS		DOCUMENT NUMBER	ISSUE DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
CJ	A01	6,566,195	May 20, 2003	Method and Structure for an Improved Floating Gate Memory Cell	438	257	February 6, 2002
CJ	A02	6,326,263	December 4, 2001	Method of Fabricating a Flash Memory Cell	438	257	August 11, 2000
CJ	A03	6,153,494	November 28, 2000	Method to Increase the Coupling Ration of Word Line to Floating Gate by Lateral Coupling in Stacked Gate of Flash	438	424	May 12, 1999

EXAMINER	<i>calvin lee</i>	DATE CONSIDERED	<i>1.26.05</i>
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